

CURRICULUM VITAE – DR. JAYESH JOGLEKAR

PERSONAL INFORMATION

Department of Electrical Engineering
Maharashtra Institute of Technology
S. No. 124, Paud Road, Kothrud
Pune - 411 038 India
Contact No: +91 20 3027 3400 Ext: 3480
Fax No: +91 20 2544 2770
Email: jayesh.joglekar@mitpune.edu.in
Website: <http://www.joglekar.in>
Mobile: +919545450645



DATE OF BIRTH

18-April-1980

EDUCATION

Doctoral degree (Ph.D.) in Electrical Engineering from the Savitribai Phule Pune University (formally University of Pune), India (2016).

Guide: Dr. Y. P. Nerkar

Thesis title: A Novel Power Grid Restoration Scheme using Active Generalized Unified Power Flow Controller

Master's degree (M.E.) in Power System from the University of Pune, India (2004).

Guide: Dr. Y. P. Nerkar

Thesis title: An Islanding Plan for Pune City

Bachelor's degree (B.E.) in Electrical Engineering from the University of Pune, India (2002).

Guide: Dr. (Mrs.) G. A. Vaidya

Thesis title: Simulation of Power System using MatLab

JOB EXPERIENCES

From August 2016 till date Associate Professor of Electrical Engineering at Maharashtra Institute of Technology, Pune India.

From August 2010 to July 2016 Assistant Professor of Electrical Engineering at Maharashtra Institute of Technology, Pune India.

From August 2008 to July 2010 Sr. Lecturer of Electrical Engineering at Maharashtra Institute of Technology, Pune India.

From August 2004 to July 2008 Lecturer of Electrical Engineering at Maharashtra Institute of Technology, Pune India.

From January 2004 to July 2004 CHB Lecturer of Electrical Engineering at College of Military Engineering, Pune India.

AREAS OF INTEREST

Micro grid Operation and Control, Grid Restoration Analysis, Modeling and Design of Power System Islanding with Micro grid.

PUBLICATIONS

PH.D. THESIS

A Novel Power Grid Restoration Scheme using Active Generalized Unified Power Flow Controller, January 2016, Savitribai Phule Pune University (formerly University of Pune). [[Available online on Shodhganga](#)]

BOOK CHAPTER

'Techno-economic issues of power quality', published by The Institution of Engineering and Technology (IET) UK, in book 'Power Quality in Future Electric Power System' (Chapter :10, pp 343-356).

JOURNALS

1. S.Heganna, J. J. Joglekar, 'Active vibration control of smart structure using PZT patches', Elsevier Procedia Computer Science, Vol. 89 (2016) pp 710 – 715. [[doi: 10.1016/j.procs.2016.06.040](#)]
2. J. J. Joglekar, Dr. N. Gopalakrishnan, Dr. Y. P. Nerkar, "A Novel Power Grid Restoration Scheme using Active PFC", Elsevier - International Journal Procedia Technology, Vol. 21 Nov 2015, pp 256-263. [[doi:10.1016/j.protcy.2015.10.024](#)]
3. J. J. Joglekar, Dr, N. Gopalakrishnan, Dr. Y. P. Nerkar, "A Novel Approach for Improving Stability of Thermal Power Stations during Cascade Tripping", Technical Journal of The Institution of Engineers (India), Vol. 37 Nov 2013, pp 119-124, ISBN 978-81-924990-1-7.
4. J. J. Joglekar and Dr. Y. P. Nerkar, A different approach in system restoration with special consideration of Islanding schemes, International Journal of Electrical Power & Energy Systems (Elsevier-Science Direct), vol. 30, issue 9, November 2008, pp: 519-524. [[doi: 10.1016/ j.ijepes.2008.04.003](#)]
5. J. J. Joglekar and Dr. Y. P. Nerkar, "A plan of Islanding scheme for Pune City", De Gruyter - International Journal on Emerging trends in Electrical Engineering, vol. 5 issue 1, 2006. [[doi: 10.2202/ 1553-779X.1107](#)]

CONFERENCE PROCEEDINGS

1. S.Heganna, J. J. Joglekar, 'Precise Positioning Application Considering Vibration Control in Piezo Actuated Stage', IEEE Third International Conference on electronics and Communication systems (ICECS'16), Coimbatore
2. J. J. Joglekar, Dr. Gopalakrishnan and Dr. Y. P. Nerkar, "A Novel Power Grid Restoration Scheme using Active PFC", International Conference on Smart Grid Technologies (ICSGT 2015), Coimbatore, 6-9 August 2015.

3. J. J. Joglekar and Dr. Y. P. Nerkar, “Application of UPFC for improving micro-grid voltage profile”, 2nd IEEE International Conference on Sustainable Energy Technologies, Kandy, Sri Lanka, 5-9 December 2010.
4. J. J. Joglekar and Dr. Y. P. Nerkar, “Analysis and Design of Power System Restoration in the Context of Generating Plant Islanding”, 2nd IEEE International Conference on Power and Energy (PECon 08), Johor Baharu, Malaysia, Dec 1-3, 2008.
5. J. J. Joglekar and Dr. Y. P. Nerkar, “Design and Development of Power Grid Restoration Scheme for Maharashtra State with Generating Plant Islanding”, IEEE International conference ‘POWERCON’ 2008, New Delhi, October 12—15, 2008.

CONSULTANCY /
FUNDED PROJECTS

1. Verification of Electrical Circuit design at 40 MLD STP, Sambalpur (Govt. of Odisha, India)
2. Design and Development of Laboratory Scale Supercapacitor (TBI, MIT Pune)
3. Analysis of Power System Restoration in context to Islanding (BCUD-University of Pune)
4. An investigation for usability of novel distribution source in smart grid (BCUD-University of Pune)

MEMBERSHIPS

1. Institution of Engineer (India)
2. Life Member, ISTE (India)
3. IEEE (USA)
4. IET (UK)
5. IEEE Smart Grid R&D Committee (USA)
6. AAAS (USA)

RESPONSIBILITIES

INSTITUTE LEVEL

1. Committee member for Industry Interaction and Finance for MIT WPU
2. Committee member for addressing academic queries for admissions in MIT WPU
3. ARC Coordinator for FE, SE and ME Admission
4. Academic Coordinator for Suryamitra Skill Development Program conducted by Ministry of New and Renewable Energy, Govt. of India.
5. Member Staff Selection Committee for SPPU / MIT ADT University
6. Coordinator for Event Record Committee
7. Coordinator for admission Counseling Committee